

S/N 09/584,566

**PATENT**

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicant: Leonard Forbes et al.  
Serial No.: 09/584,566  
Filed: May 31, 2000  
Title: HORIZONTAL MEMORY DEVICES WITH VERTICAL GATES



Examiner: Unknown  
Group Art Unit: Unknown  
Docket: 303.691US1

**INFORMATION DISCLOSURE STATEMENT**

Assistant Commissioner for Patents  
Washington, D.C. 20231

In compliance with the duty imposed by 37 C.F.R. § 1.56, and in accordance with 37 C.F.R. §§ 1.97 *et. seq.*, the enclosed materials are brought to the attention of the Examiner for consideration in connection with the above-identified patent application. Applicants respectfully request that this Information Disclosure Statement be entered and the documents listed on the attached Form 1449 be considered by the Examiner and made of record. Pursuant to the provisions of MPEP 609, Applicants further request that a copy of the 1449 form, initialed by the Examiner to indicate that all listed citations have been considered, be returned with the next official communication.

Under 37 C.F.R. § 1.97(b)(3), it is believed that no fee or certificate is required with this Information Disclosure Statement. However, if an Office Action on the merits has been mailed, the Commissioner is hereby authorized to charge any additional fees or credit any overpayment to Account No. 19-0743.

The Examiner is invited to contact the Applicants' Representative at the below-listed telephone number if there are any questions regarding this communication.

**RECEIVED**

OCT 20 2000

TECHNOLOGY CENTER 2800

Respectfully submitted,

LEONARD FORBES ET AL.

By their Representatives,

SCHWEGMAN, LUNDBERG, WOESSNER & KLUTH, P.A.  
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Minneapolis, MN 55402  
(612) 373-6913

Date 10/12/2000 By Edward J. Brooks, III

Edward J. Brooks, III  
Reg. No. 40,925

**CERTIFICATE UNDER 37 CFR 1.8:** The undersigned hereby certifies that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail, in an envelope addressed to: Commissioner of Patents, Washington, D.C. 20231, on this 13 day of October, 2000.

**Gina M. Uphus**

Name

Signature

*Gina M. Uphus*

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**COMMUNICATION CONCERNING CO-PENDING APPLICATION(S)**

Commissioner for Patents  
Washington, D.C. 20231

Applicant would like to bring to the Examiner's attention the following related co-pending application(s) in the above-identified patent application:

<u>Serial No.</u>	<u>Filing Date</u>	<u>Attorney Docket</u>	<u>Title</u>
09/584,564	05/31/2000	00303.692US1	PROGRAMMABLE MEMORY DECODE CIRCUITS WITH TRANSISTORS WITH VERTICAL GATES
09/583,584	05/31/2000	00303.683US1	FIELD PROGRAMMABLE LOGIC ARRAYS WITH TRANSISTORS WITH VERTICAL GATES

Respectfully submitted,

LEONARD FORBES ET AL.

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By Applicant's Representatives,

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By Edward J. Brooks, III

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CERTIFICATE UNDER 37 CFR 1.8: The undersigned hereby certifies that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail, in an envelope addressed to: Commissioner of Patents, Washington, D.C. 20231, on this 15 day of October, 2000.

**Gina M. Uphus**

Name

Signature

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